

AJV:kna 7/25/05 407642  
PATENT

Attorney Reference Number 6047-67518-01  
Application Number 10/731,995

**Amendments to the Specification**

Please replace the paragraph beginning at page 8, line 27 with the following rewritten paragraph:

A method of forming a package, such as the package 120, using a molding process is shown in FIG. 4. A semiconductor die 401 is bonded to a substrate 403, and wire bonds 405 are provided to electrically connect the semiconductor die 401 to the substrate 403. The wire-bonded semiconductor die 401 and the substrate 403 are placed between an upper mold half 407 and a lower mold half 409. The substrate 403 can be urged toward the lower mold half 409 using a vacuum applied through a vacuum port 415. The upper mold half 407 may also feature a vacuum port 413, and additional ports 411 can be provided.

Please replace the paragraph beginning at page 9, line 5 with the following rewritten paragraph:

With the semiconductor die 401 and the substrate 403 positioned between the mold halves 407, 409, a resin or other encapsulating material is injected into a cavity 417 that defines a package cover and a cavity 419 that defines a rib. The cavity 419 may include an area 406 between the wire bonds 405 and the semiconductor die 401. After injecting the resin, the resin is cured. The semiconductor die 401, the substrate 403, and the molded package cover and rib may then be removed from the mold halves 407, 409. In this approach, the rib and package cover are formed as a single piece (i.e., the rib and the package cover are of a unitary one-piece integrated construction). Alternatively, the rib cavity 419 may be omitted from the lower mold half 409, and a package molded without a rib. An additional molding step and an additional mold or molds can then be used to define the rib. In the FIG. 4 approach, the solder bumps may be added to the undersurface of the substrate following molding.